

US009383791B1

(12) United States Patent

Fan et al.

(45) Date of Fa

US 9,383,791 B1

(45) **Date of Patent:**

(10) **Patent No.:**

*Jul. 5, 2016

(54) ACCURATE POWER ALLOTMENT

(71) Applicant: Google Inc., Mountain View, CA (US)

(72) Inventors: **Xiaobo Fan**, Sunnyvale, CA (US);

Mark D. Hennecke, Cupertino, CA (US); Taliver Brooks Heath, Mountain

View, CA (US)

(73) Assignee: Google Inc., Mountain View, CA (US)

(*) Notice: Subject to any disclaimer, the term of this

patent is extended or adjusted under 35

U.S.C. 154(b) by 0 days.

This patent is subject to a terminal dis-

claimer.

(21) Appl. No.: 14/641,824

(22) Filed: Mar. 9, 2015

Related U.S. Application Data

- (63) Continuation of application No. 13/352,516, filed on Jan. 18, 2012, now Pat. No. 9,009,500.
- (51) **Int. Cl. G06F** 1/2

 G06F 1/26
 (2006.01)

 G06F 1/00
 (2006.01)

 G06F 19/00
 (2011.01)

G06F 9/50 (2006.01)

(52) U.S. Cl.

CPC ... **G06F 1/26** (2013.01); G06F 1/00 (2013.01); G06F 9/50 (2013.01); G06F 19/00 (2013.01)

(58) Field of Classification Search

CPC G06F 1/3203; G06F 1/00; G06F 19/00; G06F 1/26; G06F 9/50

See application file for complete search history.

(56) References Cited

U.S. PATENT DOCUMENTS

4,360,849 A 11/1982 Harris et al. 4,954,945 A 9/1990 Inoue 5,119,014 A 6/1992 Kronberg 5,339,445 A 8/1994 Gasztonyi 5,432,947 A 7/1995 Doi 5,458,991 A 10/1995 Severinsky (Continued)

FOREIGN PATENT DOCUMENTS

EP 1 439 622 7/2004 KR 10-2002-0032128 5/2002 (Continued)

OTHER PUBLICATIONS

U.S. Appl. No. 12/690,022, Sudindranath et al, filed Jan. 19, 2010. (Continued)

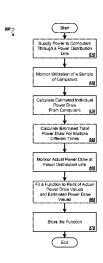
Primary Examiner — Jaweed A Abbaszadeh
Assistant Examiner — Terrell Johnson

(74) Attorney, Agent, or Firm — Fish & Richardson P.C.

(57) ABSTRACT

The subject matter of this specification can be embodied in, among other things, a method that includes supplying power to a portion of a data center through a power distribution line. Utilization of a statistically significant sample of the computers is monitored, and an estimated individual power draw for each of the computers based on the utilization is calculated. An estimated total power draw is calculated for different times from the estimated individual power draws to generate a collection of estimated total power draw values for the different times. Actual power draw is monitored at the power distribution line and a collection of actual power draw values is generated. A function is fitted to pairs of actual power draw values and estimated power draw values, each pair comprising an actual draw value and an estimated draw value for the same time, and the function is then stored.

20 Claims, 8 Drawing Sheets



US 9,383,791 B1 Page 2

(56)			Referen	ces Cited	7,203,943		4/2007 4/2007	Shaffer
		U.S. I	PATENT	DOCUMENTS	7,210,048 7,222,030	B2	5/2007	Banginwar et al.
					7,222,246			Pomaranski et al.
	5,552,640 5,579,522			Sutton et al.	7,228,441 7,240,225		6/2007 7/2007	Brewer et al.
	5,694,307			Christeson et al. Murugan	7,242,945		7/2007	Reddi
	5,694,607	A	12/1997	Dunstan et al.	7,272,517			Brey et al.
	5,828,568			Sunakawa et al.	7,272,733 7,278,273			Pomaranski et al. Whitted et al.
	5,844,884 5,856,776		1/1998	Armstrong et al.	7,305,282		12/2007	
	5,952,617			Bergstrom	7,313,503			Nakagawa et al.
	6,009,177		12/1999		7,349,828 7,383,137			Ranganathan et al. Hanebutte
	6,055,640 6,141,192		10/2000	Kageshima et al. Garzon	7,383,454			Loffink et al.
	6,223,205	B1	4/2001	Harchol-Balter et al.	7,420,293			Donnelly et al. Bahali et al.
	6,289,684 6,345,240		9/2001 2/2002	Guidry, II et al.	7,421,599 7,430,675		9/2008	
	6,374,627			Schumacher et al.	7,444,526	B2	10/2008	Felter et al.
	6,429,706	В1	8/2002	Amin et al.	7,457,976			Bolan et al.
	6,487,509 6,496,366		11/2002	Aisa Coglitore et al.	7,461,273 7,461,274		12/2008	Moore et al. Merkin
	6,506,111			Sharp et al.	7,467,306	B2	12/2008	Cartes et al.
	6,513,145	B1	1/2003	Venkitakrishnan	7,467,311			Bahali et al. Baba et al.
	D473,225 6,559,559		4/2003 5/2003	Constru	7,469,351 7,487,058			Gross et al.
	D475,705			Coglitore et al.	7,493,235	B2	2/2009	Artman et al.
	6,584,559	B1	6/2003	Huh	7,493,503			Aldereguia et al.
	6,594,771 6,625,737		7/2003 9/2003	Koerber et al.	7,496,772 7,499,279			Nguyen Campbell et al.
	6,652,373			Sharp et al.	7,511,959	B2	3/2009	Belady et al.
	6,652,374	B2	11/2003	Sharp et al.	7,511,960 7,514,815			Hillis et al. Paik et al.
	6,667,891 6,668,565			Coglitore et al. Johnson et al.	7,525,207			Clidaras et al.
	6,721,672			Spitaels et al.	7,529,086			Fink et al.
	6,741,467			Coglitore et al.	7,549,069 7,549,070			Ishihara et al. Zwinger et al.
	6,768,222 6,770,810		7/2004 8/2004	Wiebe et al.	7,555,666			Brundridge et al.
	6,775,787		8/2004	Greene	7,561,977			Horst et al.
	6,785,827			Layton et al.	7,581,125 7,595,642		8/2009 9/2009	Ranganathan et al.
	6,804,616 6,822,859		10/2004	Coglitore et al.	7,633,181			Gross et al.
	6,824,362	B2	11/2004	Dodson, III	7,644,148			Ranganathan et al.
	6,834,354 6,847,995		1/2004	Togawa Hubbard et al.	7,646,590 7,647,516			Corhodzic et al. Ranganathan et al.
	6,850,408			Coglitore et al.	7,675,740	B2	3/2010	Rasmussen et al.
	6,859,366	B2	2/2005	Fink	7,698,580 7,701,714			Schindler et al. Shabany
	6,867,570 6,880,349			Vithayathil et al. Johnson et al.	7,701,714			Goodrum et al.
	6,901,521			Chauvel et al.	7,705,484	B2	4/2010	Horst
	6,904,534			Koenen	7,726,144 7,756,652		6/2010	Larson Lewis et al.
	6,907,395 6,915,438		6/2005 7/2005	Hunt et al.	7,774,630			Hatasaki et al.
	6,935,130	B2		Cheng et al.	7,783,906	B2		Turner et al.
	6,937,947	B2	8/2005	Trembley	7,783,909 7,818,594			Hatasaki et al. Gorbatov et al.
	6,967,283 6,969,922			Rasmussen et al. Welches et al.	7,825,536	B2		Jacobson et al.
	6,980,433	B2	12/2005	Fink	7,827,421			Brewer et al.
	6,986,066 7,010,363			Morrow et al. Donnelly et al.	7,840,824 7,844,839			Baba et al. Palmer et al.
	7,010,303			Yang et al.	7,856,549	B2	12/2010	Wheeler
	7,011,576			Sharp et al.	7,863,769 7,904,287			Busdiecker et al. Lefurgy et al.
	7,020,586 7,036,035			Snevely Allison et al.	7,996,839			Farkas et al.
	7,046,514	B2		Fink et al.	8,010,215			Borkenhagen et al.
	7,058,524			Hayes et al.	8,091,087 8,178,997			Ali et al. Talkin et al.
	7,082,541 7,099,784			Hammond et al. Spitaels et al.	8,214,843	B2		Boss et al.
	7,123,477	B2	10/2006	Coglitore et al.	8,595,515			Weber et al.
	7,127,625			Farkas et al.	8,601,287 2001/0003207			Weber et al. Kling et al.
	7,136,725 7,145,772		12/2006	Paciorek et al. Fink	2001/0003207		1/2002	
	7,161,557	B2	1/2007	Thornton	2002/0065049	$\mathbf{A}1$	5/2002	Chauvel et al.
	7,166,930		1/2007		2002/0194517			Cohen et al. Cohen et al.
	7,173,820 7,173,821			Fink et al. Coglitore	2003/0005339 2003/0048647			Sadler et al.
	7,178,147	B2	2/2007	Benhase et al.	2003/0055969	A 1	3/2003	Begun et al.
	7,203,849		4/2007		2003/0133263		7/2003	
	7,203,852	В2	4/2007	Cohen et al.	2003/0197428	Al	10/2003	Hatton et al.

U.S. PATENT DOCUMENTS 2010-0039308 A1 2000 Althourter at 2010-0039308 A1 4 22010 Roberton and 2010-0039308 A1 4 22010 Roberton and 2010-0039308 A1 4 22010 Roberton and 2010-0039308 A1 4 22010 Baldwajs et al. 2010-0103174 A1 4 22010 Roberton and 2010-0039307 A1 8 2000 Roberton A1 8 2004 Roberton A1	(56)	References Cited	2009/0259345 A1 10/2009 Kato et al.
2003/0217068 AI 122004 Manis et al. 2010/0102024 AI 42010 Bachright et al. 2010/0102024 AI 42010 42010 Allerthe et al. 2010/0102024 AI 42010 Allerthe	II S	PATENT DOCUMENTS	2009/0281846 A1 11/2009 Rose 2010/0050008 A1 2/2010 Allalouf et al.
2004-0002224 Al 22004 Bash et al. 2010-0102592 Al 42010 Bash et al. 2010-01031474 42010 Bash et al. 2010-01031482 42010 Ba	0.5.	TATEN DOCUMENTS	
200400002538 Al 22004 Sank et al. 2010031887 Al 10210 Sank et al. 20100318887 Al 1021003 Sank et al. 20100318887 Al 1021003 Sank et al. 20100318887 Al 20100318887	2003/0237008 A1	12/2003 Freevol et al	
2004-0000224 Al 22004 Spatials et al. 2010-0005882 Al 122010 Spatials et al. 2010-00018827 Al 122010 Spatials et al. 2010-0005827 Al 122004 Spatials et al. 2010-00018827 Al 122010 Sp			
2004-0167373 Al \$2004 Spirited et al. 2010-0318827 Al 127010 Elining Goff 113040 2004-0127334 Al 112004 Fink et al. 2010-0318828 Al 127010 Elining Goff 113040 2004-012734 Al 127004 Ciglitor 2004-012734 Al 127004 Ciglitor 2004-012734 Al 127005 Exhand et al. 2012-013882 Al 52012 Elining Goff 113040 2005-0002734 Al 127004 Elining Ciglitor 2005-0002734 Al 127005 Exhand et al. 2012-014819 Al 52012 Elining Ciglitor 2005-0002734 Al 2005 Enhance et al. 2012-014819 Al 52012 Elining Ciglitor 2005-0002734 Al 2005 Elining Ciglitor 2		2/2004 Bash et al.	
2004-0184273 Al \$2004 Flink 2010-018827 Al 122010 Shah et al. 2010-01823 Al 122010 Shah et al. 2012-01823 Al 122010 Shah et al. 2012-01823 Al 2012-0	2004/0150374 A1		
2004-0223687 A1 12004 Fink 2010-031828 A1 * 1222010 Elfing G69F 1303-04 2004-0223687 A1 12004 Elfing G713-3-40 2004-0223687 A1 12005 Elfing G713-3-40 2004-0223687 A1 12005 Elfing G713-3-40 2004-0223687 A1 12005 Elfing G713-3-40 2004-022368 A1 12005 Elfing G713-3-40 2004-022368 A1 12005 Elfing G713-3-40 2005-0205-0205-0205 A1 12005 Elfing G713-3-40 2005-0205-0205-0205 A1 12005 Elfing G713-3-40 2005-0205-0205-0205-0205-0205-0205-0205			
2004-0223306 XI 11/2004 Finak et al. 2011-0106326 Al. 5/2011 Anumobi et al. 2012-00063079 Al. 3/2012 Brigone et al. 2012-00063079 Al. 3/2012 Brigone et al. 2012-01063079 Al. 2005-000790 Al. 3/2005 Brigone et al. 2012-0106308 Al. 2/2012 Chaple et al. 2012-0106308 Al. 2/2013 Chaple et al. 2012-0106308 Al. 2/2012 Chaple et al. 2012-0106308 Al. 2/2013 Al. 2/2012 Chaple et al. 2012-0106308 Al. 2/2013 Al. 2/2012 Chaple et al. 2012-0106308 Al. 2/2013 Al. 2/2012 Chaple et al.			
2004/02/3662 Al 12/2004 Editurd 2011/01/03/36 Al 5/2011 Anumbol et al. 2012/00/65/39 Al 12/2005 Editurd 2012/00/65/39 Al 12/2005 Editurd 2012/00/65/39 Al 2012/39 Al 2012/			
2004/02/3766 A1 22/2004 Editinal 2012/00/3004 3.2/2012 Horsub et al. 2012/01/300 A1 5.2/2012 Salababour et al. 2012/01/300 A1 5.2/2012 Gats			
Double D			
20050070529 A1 32005 Farkas et al. 2012033086 A1 * 12/2012 Gatts			
2005/09/109/3 Al 3/2005 Farks et al 2012/0330586 Al * 12/2012 Gatts G00F 1/3/203 2005/09/109/3 Al 3/2005 Devlin Controller et al 2005/09/109/3 Al 5/2005 Fink KR 10/2008/09/109/3 Al 5/2005 Fink KR 10/2008/09/109/3 Al 5/2005 Fink KR 10/2008/09/3 Al 5/2005 Fink KR 10/2008/09/3 Al 3/2001 Albas et al WO 0.17/20496 3/2001 2005/09/107/3 Al 8/2005 Fink KR 10/2008/09/3 3/2001 WO 2007/08/235 6/2007 7/2007 2005/01/107/3 Al 8/2005 Fing 2005/01/107/3 Al 8/2005 Fing 2005/01/107/3 Al 8/2005 Fing 2005/01/107/3 Al 8/2005 Fing 2005/01/107/3 Al 8/2005 Fink Al 2005/01/107/3 Al 8/2005 Fink Al 2005/01/107/3 Al 8/2005 Fink Al 2006/00/205/3 Al 1/2006 Fink Al 2006/00/205/3 Al 1/2007 Al 8/2005 Fink Al 2006/00/205/3 Al 1/2006 Fink Al 2006/00/205/3 Al			
2005/0009730 Al 2005 Sevent Commondered 2005/00099730 Al 2005 Sevent Commondered			
2005/0909750 A 2005 Secondary A 2006 Secondary A 2007 Secondary A 2006 Secondary A 2007 Secondary A 2			
2005/090976 Al 52005 Fink FIGREIGR PATENT DOCUMENTS			
2005/0909770 Al 5/2005 Fink KR 10-2008-0019938 5/2008			FOREIGN PATENT DOCUMENTS
2005/0102544 Al 5/2005 Street et al. WO 2007/08/2456 6/2007	2005/0099750 A1		
2005/0132234 A 6/2005 Fairlie			
2005/0167373 Al 2005 Sohnson et al. 2005/0171733 Al 2005 Sohnson et al. 2005/0171735 Al 2005 Sohnson et al. 2005/0171735 Al 2005 Foung 2005/0171735 Al 2005/01			
2005/01/7755 Al 2005 Rosing et al.			
2005/01/7153 Al 2005 Soing et al.			WO 2007/082331 7/2007
2005/020093 A1 2005 Winn et al. 2005 Winn et al. 2005/020093 A1 10/2005 Winn et al. 2005/020093 A1 10/2005 Winn et al. 2006/000763 A1 1/2006 Fink 2006/000763 A1 1/2006 Winn et al. 2006/000763 A1 1/2006 Winnshagans et al. 2006/000763 A1 1/2006 Winnshagans et al. 2006/000763 A1 1/2007 Winnshagans et al. 2007/001681 A1 1/2007 Winnshagans et al. 2007/005064 A1 3/2007 Winnshagans et al. 2007/005064 A1 4/2007 Winnsh			OTHER PUBLICATIONS
2005/020934 Al 10/2005 Patterson et al. 2006 Fink 2006 F	2005/0177755 A1		
2006/0007653 Al 1/2006 Hung 2006/00082263 Al 2/2006 Hung 2006/0008297 Al 4/2006 Courney 2009/0008379 Al 4/2006 Courney 2009/0008379 Al 4/2006 Entire et al. 2006/0018763 Al 8/2006 Entire et al. 2006/0018763 Al 8/2006 Entire et al. 2007/0018414 Al 1/2007 Entire et al. 2007/0018416 Al 2/2007 Entire et al. 2007/0018418 Al 2/2007 Entire et al. 2/2007/0018418 Al 2/2007 Entire et al. 2/2008/0018407 Al 2/2008 Entire et al. 2/2008/0018407 Al 2/2008 Entire et al. 2/2008 Entire et al. 2/2008 Entire			"Protect High-Density Equipment From Thermal Damage—Guar-
2006/002832 Al 1/2006 Rimier et al. 2006/0088597 Al 4/2006 Rimier et al. 2006/0088593 Al 2/2006 Rimier et al. 2006/0088593 Al 2/2006 Rimier et al. 2006/008849 Al 2/2006 Rimier et al. 2/2007 Rimier			anteed," APC Currents, Sep. 2007, 18(4):3 pages.
2006/082563 Al			'Advanced Micro Devices, Inc.,' [online] "AMD Opteron Processor
2006/081597 Al 4/2006 Countrey			with Direct Connect Architecture," Sep. 2006 [retrieved on Sep. 3,
2006/0107262 Al 5/2006 Fink et al 2006/0137636 Al 8/2006 Fink et al 2006/0137636 Al 8/2006 Fink et al 2006/0138449 Al 1/2007 2007/0016814 Al 2/2007 2007/0038414 Al 2/2007 2007/0058644 Al 3/2007 2007/0058644 Al 4/2007 Elink 2007/0088644 Al 4/2007 Elink 2007/0143760 Al 6/2007 Chan et al 2007/0143760 Al 6/2007 Spitaels 4/2007 4/2007 2008/0088030078 Al 2/2008 2008/0168383 Al 8/2007 2008/0168383 Al 2/2007 2008/0168361 Al 2/2007 Elink 2/2008 2/2008 Elink 2/2008 2/2008 2/2008 Elink 2/2008			2009]. Retrieved from the Internet: http://enterprise.amd.com/down-
2006/0187636 Al 8.2006 Fink et al. 2006/0187636 Al 1	2006/0098371 A1		loads/2P_Power_PID_41497, 2 pages.
2006/0259622 A1			
2006/0284489 Al 12/2007 2007/0018414 Al 2/2007 2007/0038414 Al 2/2007 2007/0050644 Al 3/2007 2007/0050644 Al 3/2007 2007/0050647 Al 3/2007 3/2007 2007/0050647 Al 3/2007 2007/0050647 Al 3/2007 2007/0076373 Al 4/2007 Fink 2007/0083634 Al 4/2007 Ellis et al. 2007/018363 Al 4/2007 Ellis et al. 2007/018363 Al 4/2007 Ellis et al. 2007/018363 Al 4/2007 Ellis et al. 2007/0198383 Al 2007/018383 Al 2007/01809078 Al 2/2008 Ellis et al. 2007/020920 Al 9/2007 Elshimar et al. 2007/020920 Al 9/2007 Elshimar et al. 2008/0309979 Al 2/2008 Bridges et al. 2008/0030978 Al 2/2008 Bridges et al. 2008/003023487 Al 2/2008 Ellis et al.			PWM Fan Controller," 2003, [retrieved on Oct. 24, 2006]. Retrieved
2007/0016814 A 1 1/2007 Rusu et al.			from the Internet: www.analog.com/UploadedFiles/Data_Sheets/
2007/003644 A1 3/2007 Merkin 3/2007 Conroy			ADM1030.pdf#xml=http://search.analog.com/search/pdfPainter.
2007/0050647 A1 * 3/2007 Conroy G06F J/3203			
Taylogo			· · · · · · · · · · · · · · · · · · ·
2007/0052294 Al 3/2007 Kraus Fink	2007/0050647 A1*		
2007/0076373 A.1 4/2007 Fink	2007/0052294 A1		
2007/0889446 A1 4/2007 Larson et al. 2007/0143760 A1 6/2007 Chan et al. 2007/0150215 A1* 6/2007 Spitaels			
2007/0143760 A1 6/2007 Chan et al. 2007/0150215 A1 6/2007 Chan et al. 3pitaels			e e e e e e e e e e e e e e e e e e e
2007/0150215 A1 * 6/2007 Spitaels Mo2J 3/14 702/61 To2/61 T			
Retrieved from the Internet: http://download.intel.com/design/intarch/datashts/31139101, 72 pages.			
2007/0168161 A1	2007/0130213 AT		
2007/0220292 A1 9/2007 Ishihara et al. Fan Drive to Linear and Reduces Acoustic Noise," 2006, [retrieved on Oct. 23, 2006]. Retrieved from the Internet: www.maxim-ic.com/appnotes.cfm/an_pk/3530, 3 pages. Oct. 23, 2006]. Retrieved from the Internet: www.maxim-ic.com/appnotes.cfm/an_pk/3530, 3 pages. Oct. 23, 2006]. Retrieved from the Internet: www.maxim-ic.com/appnotes.cfm/an_pk/3530, 3 pages. Oct. 23, 2006]. Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://download.micron.com/pdf/technotes/ddr/Internet: http://download.micron.com/pdf/technotes/ddr/Internet: http://download.micron.com/pdf/technotes/ddr/Internet: http://www.segages. Oct. 23, 2006]. Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://download.micron.com/pdf/technotes/ddr/Internet: http://download.micron.com/pdf/technotes/ddr/Internet: http://download.micron.com/pdf/technotes/ddr/Internet: http://www.segages. Oct. 23, 2006]. Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://www.segages. Oct. 23, 2006]. Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://www.segages. Oct. 23, 2006]. Retrieved on Sep. 3, 2009]. Retrieved on Sep. 3, 2009]. Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://www.segages. Oct. 23, 2006]. Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://www.segages. Oct. 23, 2006]. Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://www.segages. Oct. 23, 2006]. Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://www.segages. Oct. 23, 2006]. Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://www.segages. Oct. 23, 2006]. Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://www.segages. Oct. 23, 2006]. Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://www.segages. Oct. 23, 2006]. Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://www.segages. Oct. 23, 2006]. Retrieved on Sep. 3, 2009]. Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://ww			intarch/datashts/31139101, 72 pages.
2007/0291433			
2007/0291433			
2008/0039078 A1 2/2008 Whitted et al. 2/2008 Whitted et al. 2/2008 Whitted et al. 2/2008 Bridges et al. 2/2008 Bridges et al. 2/2008 Hillis et al. 2/2008 Whitted et al. 2/2008 Hillis et al. 2/2008 Hillis et al. 2/2008 Whitted et al. 2/2008 Hillis et al. 2/2008 Whitted et al. 2/2008 Hillis et al. 2/2008 Whitted et al. 2/2008 Whitted et al. 2/2008 Hillis et al. 2/2008 Whitted et al. 2/20			, ,
2008/0039979 A1 2/2008 Bridges et al. Power for DDR," 2001 [Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://download.micron.com/pdf/technotes/ddr/ TN4603, 26 pages. Yates et al. Yates et			
2008/0062647 Al 3/2008 Hillis et al.			
2008/0064317 A1 3/2008 Yates et al. TN4603, 26 pages. 2008/0094797 A1 4/2008 Coglitore et al. 'National Semiconductor' [online]. "LM64 -+-1C Remote Diode 2008/0106241 A1 5/2008 Deaver et al. Temperature Sensor with PWM Fan Control and 5 GPIO's," 2006, 2008/0114997 A1 5/2008 Chin Iretrieved on Oct. 23, 2006]. Retrieved from the Internet: http://www. 2008/015312 A1 7/2008 Dettinger et al. 'Seagate Technology LLC' [online] "Product Manual Barracuda 2008/0172312 A1 7/2008 Synesiou et al. Synesiou et al. Internet: http://www.seagate.com/support/disc/manuals/ata/ 2008/0278003 A1 11/2008 Pouchet et al. 'Slashdot' [online]. "Building an Energy Efficient Datacenter?" 2009/0050591 A1 2/2009 Hart et al. 'SUN' [online] "Project Blackbox-Scenarios," Retrieved Oct. 19, 2009/0138100 A1 5/2009 Khorramshahi 'SUN' [online] "Project Blackbox" [retrieved Oct. 19, 2006]. 2009/0138313 A1 5/2009 Morgan et al. 2006, Retrieved from the Internet: http://www.sun.com/emrkt/blackbox/in-			
2008/0094797 A1 4/2008 Coglitore et al. 'National Semiconductor' [online]. "LM64 -+-1C Remote Diode 2008/0106241 A1 5/2008 Deaver et al. Temperature Sensor with PWM Fan Control and 5 GPIO's," 2006, 2008/0123288 A1 5/2008 Chin Iretrieved on Oct. 23, 2006]. Retrieved from the Internet: http://www. elecdesign.com/Articies/Index.cfm? ArticleID=6281, 3 pages. 2008/015312 A1 7/2008 Dettinger et al. Synesiou et al. 'Seagate Technology LLC' [online] "Product Manual Barracuda 7200.7," Sep. 2005 [Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://www.seagate.com/support/disc/manuals/ata/cuda7200pm, 54 pages. 2008/0278003 A1 11/2008 Pouchet et al. Synesiou et al. Slashdot' [online]. "Building an Energy Efficient Datacenter?" [retrieved on Sep. 6, 2007]. 1997-2007, 25 pages. 2009/0070611 A1 3/2009 Bower et al. S'UN' [online] "Project Blackbox-Scenarios," Retrieved Oct. 19, 2009/0138219 A1 5/2009 Khorramshahi 'SUN' [online] "Project Blackbox" [retrieved Oct. 19, 2006]. Retrieved from the Internet: http://www.sun.com/emrkt/blackbox/in- 2009/0138313 A1 5/2009 Baba et al. SVIN' [online] "Project Blackbox" [retrieved Oct. 19, 2006]. Retrieved from the internet http://www.sun.com/emrkt/blackbox/in-			TN4603, 26 pages.
2008/0106241 A1 5/2008 Deaver et al. Temperature Sensor with PWM Fan Control and 5 GPIO's," 2006, 2008/0123288 A1 5/2008 Chin [retrieved on Oct. 23, 2006]. Retrieved from the Internet: http://www. 2008/0165714 A1 5/2008 Hillis elecdesign.com/Articies/Index.cfm? ArticleID=6281, 3 pages. 2008/0168282 A1 7/2008 Dettinger et al. "Seagate Technology LLC" [online] "Product Manual Barracuda 7200.7," Sep. 2005 [Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://www.seagate.com/support/disc/manuals/ata/cuda7200pm, 54 pages. 2008/0278003 A1 11/2008 Pouchet et al. "Slashdot" [online]. "Building an Energy Efficient Datacenter?" [retrieved on Sep. 6, 2007]. 1997-2007, 25 pages. 2009/0138100 A1 5/2009 Bower et al. "SUN" [online] "Project Blackbox-Scenarios," Retrieved Oct. 19, 2006]. Retrieved from the Internet: http://www.sun.com/emrkt/blackbox/in- 2009/0138313 A1 5/2009 Morgan et al. 2006, Retrieved from the Internet: http://www.sun.com/emrkt/blackbox/in-			
2008/0123288 A1 5/2008 Hillis elecdesign.com/Articles/Index.cfm?ArticleID=6281, 3 pages. 2008/0165714 A1 7/2008 Dettinger et al. 'Seagate Technology LLC' [online] "Product Manual Barracuda 2008/0165714 A1 7/2008 Draw in the Internet: Brundridge 7/2008 Synesiou et al. 2008/0172312 A1 7/2008 Synesiou et al. 1/2008 Synesiou et al. Internet: http://www.seagate.com/support/disc/manuals/ata/cuda7200pm, 54 pages. 2008/0278003 A1 11/2008 Pouchet et al. 2/2009 Hart et al. 'Slashdot' [online]. "Building an Energy Efficient Datacenter?" 2009/0070611 A1 3/2009 Bower et al. 'SUN' [online] "Project Blackbox-Scenarios," Retrieved Oct. 19, 2009/0138100 A1 5/2009 Bletsch et al. 2009/0138313 A1 5/2009 Morgan et al. 2009/0138313 A1 5/2009 Morgan et al. 2009/0187776 A1 2/2009 Baba et al. 'SUN' [online] "Project Blackbox" [retrieved Oct. 19, 2006]. 2009/0195977 A1 8/2009 Fink et al. Retrieved from the internet http://www.sun.com/emrkt/blackbox/in-		5/2008 Deaver et al.	
2008/0165714 A1 7/2008 Dettinger et al. Seagate Technology LLC' [online] "Product Manual Barracuda 7/2008 Synesiou et al. 2008/0172312 A1 7/2008 Synesiou et al. 2008/0278003 A1 11/2008 Pouchet et al. 2009/0050591 A1 2/2009 Hart et al. 2009/0070611 A1 3/2009 Bower et al. 2009/0138100 A1 5/2009 Synesiou et al. 2009/0138219 A1 5/2009 Bletsch et al. 2009/0138313 A1 5/2009 Bletsch et al. 2009/0138313 A1 5/2009 Baba et al. 2009/0195977 A1 8/2009 Fink et al. Retrieved from the internet http://www.sun.com/emrkt/blackbox/in-			[retrieved on Oct. 23, 2006]. Retrieved from the Internet: http://www.
2008/0168282 A1 7/2008 Brundridge 7/2008 Synesiou et al. 7/2007," Sep. 2005 [Retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://www.seagate.com/support/disc/manuals/ata/cuda7200pm, 54 pages. 2009/00750591 A1 2/2009 Hart et al. "Slashdot' [online]. "Building an Energy Efficient Datacenter?" 2009/0138100 A1 5/2009 Bower et al. "SUN' [online] "Project Blackbox-Scenarios," Retrieved Oct. 19, 2009/0138219 A1 5/2009 Morgan et al. 2006, Retrieved from the Internet: http://www.sun.com/emrkt/blackbox/scenarios.jsp, 7 pages. 2009/0187776 A1 7/2009 Baba et al. "SUN' [online] "Project Blackbox" [retrieved Oct. 19, 2006]. 2009/0195977 A1 8/2009 Fink et al. Retrieved from the internet http://www.sun.com/emrkt/blackbox/in-			
2008/0172312 A1 7/2008 Synesiou et al. 7/2008 1/2007/, Sep. 2005 [Retrieved on Sep. 3, 2009]. Retrieved from the Internet: 2008/0234873 A1 9/2008 Gorbatov et al. Internet: http://www.seagate.com/support/disc/manuals/ata/cuda7200pm, 54 pages. 2009/050591 A1 2/2009 Hart et al. 'Slashdot' [online]. "Building an Energy Efficient Datacenter?" 2009/070611 A1 3/2009 Bower et al. [retrieved on Sep. 6, 2007]. 1997-2007, 25 pages. 2009/0138100 A1 5/2009 Khorramshahi 'SUN' [online] "Project Blackbox-Scenarios," Retrieved Oct. 19, 2006]. 2009/0138313 A1 5/2009 Morgan et al. 2006, Retrieved from the Internet: http://www.sun.com/emrkt/blackbox/in- 2009/0195977 A1 8/2009 Fink et al. Retrieved from the internet http://www.sun.com/emrkt/blackbox/in-			
2008/0234873 A1 9/2008 Gorbatov et al. Internet: http://www.seagate.com/support/disc/manuals/ata/cuda7200pm, 54 pages. 2008/0278003 A1 11/2008 Pouchet et al. cuda7200pm, 54 pages. 2009/0050591 A1 2/2009 Hart et al. 'Slashdot' [online]. "Building an Energy Efficient Datacenter?" 2009/0138100 A1 5/2009 Bower et al. [retrieved on Sep. 6, 2007]. 1997-2007, 25 pages. 2009/0138219 A1 5/2009 Bletsch et al. 2006, Retrieved from the Internet: http://www.sun.com/emrkt/ 2009/0138313 A1 5/2009 Morgan et al. blackbox/scenarios.jsp , 7 pages. 2009/0187776 A1 7/2009 Baba et al. 'SUN' [online] "Project Blackbox" [retrieved Oct. 19, 2006]. 2009/0195977 A1 8/2009 Fink et al. Retrieved from the internet http://www.sun.com/emrkt/blackbox/in-			
2009/0050591 A1 2/2009 Hart et al. "Slashdot [online]. "Building an Energy Efficient Datacenter?" 2009/0070611 A1 3/2009 Bower et al. [retrieved on Sep. 6, 2007]. 1997-2007, 25 pages. 2009/0138100 A1 5/2009 Khorramshahi "SUN" [online] "Project Blackbox-Scenarios," Retrieved Oct. 19, 2006, Retrieved from the Internet: http://www.sun.com/emrkt/blackbox/scenarios.jsp, 7 pages. 2009/0138313 A1 5/2009 Morgan et al. blackbox/scenarios.jsp, 7 pages. 2009/0187776 A1 7/2009 Baba et al. "SUN" [online] "Project Blackbox" [retrieved Oct. 19, 2006]. 2009/0195977 A1 8/2009 Fink et al. Retrieved from the internet http://www.sun.com/emrkt/blackbox/in-			
2009/0070611 A1 3/2009 Bower et al. [retrieved on Sep. 6, 2007]. 1997-2007, 25 pages. 2009/0138100 A1 5/2009 Khorramshahi 'SUN' [online] "Project Blackbox-Scenarios," Retrieved Oct. 19, 2009/0138219 2009/0138313 A1 5/2009 Bletsch et al. 2006, Retrieved from the Internet: http://www.sun.com/emrkt/blackbox/scenarios.jsp, 7 pages. 2009/0187776 A1 7/2009 Baba et al. 'SUN' [online] "Project Blackbox" [retrieved Oct. 19, 2006]. 2009/0195977 A1 8/2009 Fink et al. Retrieved from the internet http://www.sun.com/emrkt/blackbox/in-			
2009/0138100 A1 5/2009 Khorramshahi SUN' [online] "Project Blackbox-Scenarios," Retrieved Oct. 19, 2009/0138219 A1 5/2009 Bletsch et al. 2006, Retrieved from the Internet: http://www.sun.com/emrkt/blackbox/scenarios.jsp, 7 pages. 2009/0187776 A1 7/2009 Baba et al. SUN' [online] "Project Blackbox" [retrieved Oct. 19, 2006]. 2009/0195977 A1 8/2009 Fink et al. Retrieved from the internet http://www.sun.com/emrkt/blackbox/in-			
2009/0138219 A1 5/2009 Bletsch et al. 2006, Retrieved from the Internet: http://www.sun.com/emrkt/ 2009/0138313 A1 5/2009 Morgan et al. blackbox/scenarios.jsp , 7 pages. 2009/0187776 A1 7/2009 Baba et al. "SUN" [online] "Project Blackbox" [retrieved Oct. 19, 2006]. 2009/0195977 A1 8/2009 Fink et al. Retrieved from the internet http://www.sun.com/emrkt/blackbox/in-			
2009/0138313 A1 5/2009 Morgan et al. blackbox/scenarios.jsp , 7 pages. 2009/0187776 A1 7/2009 Baba et al. 'SUN' [online] "Project Blackbox" [retrieved Oct. 19, 2006]. 2009/0195977 A1 8/2009 Fink et al. Retrieved from the internet http://www.sun.com/emrkt/blackbox/in-			
2009/0187776 A1 7/2009 Baba et al. "SUN' [online] "Project Blackbox" [retrieved Oct. 19, 2006]. 2009/0195977 A1 8/2009 Fink et al. "Retrieved from the internet http://www.sun.com/emrkt/blackbox/in-			•
2009/0195977 A1 8/2009 Fink et al. Retrieved from the internet http://www.sun.com/emrkt/blackbox/in-			
2009/0241578 A1 10/2009 Carlson et al. dex.jsp, 3 pages.			
	2009/0241578 A1	10/2009 Carlson et al.	dex.jsp, 3 pages.

(56) References Cited

OTHER PUBLICATIONS

'T10 Project 1142D—Working Draft Scsi Parallel Interface-2' [online] American National Standard, Apr. 13, 1998, [retrieved on Oct. 23, 2006]. Retrieved from the Internet: www.t10.org/ftp/t10/drafts/spi2/spi2r20b, 3 pages.

"Advanced Power Management with Dell OpenManagement Power Center," Dell Inc., Mar. 2012, 21 pages.

"Cisco Data Center Infrastructure 2.5 Design Guide," Cisco Systems, Inc., Dec. 6, 2007, 180 pages.

"Data Center Design and Implementation with Cisco Catalyst 6500 Services Modules," Cisco Systems, Inc., 2004, 88 pages.

"Data Center Power and Cooling—White Paper," Cisco, Aug. 2011, 25 pages.

"Federal Energy Regulatory Commission Staff Preliminary Assessment of the North American Electric Reliability Council's Proposed Mandatory Reliability Standards," Department of Energy-Federal Energy Regulatory Commission, May 11, 2006, 137 pages.

"HP Power Advisor Utility: A Tool for Estimating Power Requirements for HP ProLiant Server Systems—Technology Brief," Hewlett Packard, Aug. 2009, 22 pages.

"Optimizing Facility Operation in High Density Data Center Environments—Technology Brief," Hewlett Packard, Aug. 2007, 26 pages.

"Power Factor Correction: A Guide for the Plant Engineer," Cutler-Hammer, Jul. 2004, 26 pages.

"Principles for Efficient and Reliable Reactive Power Supply and Consumption," Federal Energy Regulatory Commission Staff Report, Feb. 4, 2005, 177 Pages.

"Principles for Efficient and Reliable Reactive Power Supply and Consumption," United States of America Before the Federal Energy Regulatory Commission, Mar. 8, 2005, 8 pages.

"Reducing Data Center Energy Consumption," Intel Corporation, White Paper, 2008, 8 pages.

"Regulatory Studies Program," Mercatus Center at George Mason University, 13 pages.

"Unified Physical Infrastructure (UPI) Strategies for Data Center Networking," Panduit, White Paper, Jul. 2007, 15 pages.

"Your Data Center: Powered by Sentilla," Sentilla Corporation, 2012, 4 pages.

Barroso et al., "Web Search for a Planet: The Google Cluster Architecture," IEEE Micro, Mar./Apr. 2003, 23(2):22-28.

Barroso, "The Price of Performance: An Economic Case for Chip Multiprocessing," ACM Queue, Sep. 2005, 3(7):49-53.

Bean et al., "Proper Sizing of IT Power and Cooling Loads White Paper" The Green Grid, 2009, 10 pages.

Bohrer et al., "The Case for Power Management in Web Servers," Power Aware Computing, 2002, pp. 261-289.

Burkhart, "FERC Takes on Reactive Power," Spark, Mar. 2005, Letter 15, 10 pages.

Carrera et al., "Conserving Disk Energy in Network Servers," ICS 2003: Proceedings of the 17th annual international conference on Supercomputing, Jun. 23-26, 2003, 14 pages.

Chan and More, "Measuring Power in your Data Center," [White Paper], Uptime Institute IT Symposium, 2009, 5 pages.

Chase et al., "Managing Energy and Server Resources in Hosting Centers," SOSP 2001: Proceedings of the eighteenth ACM symposium on Operating systems principles, 2001, pp. 103-116.

Computers and Knurr, "Energy Efficient Infrastructures for Data Centers," White Paper, Jul. 2007, 16 pages.

Contreras and Martonsi, "Power Prediction for Intel XScale Processors Using Performance Monitoring Unit Events," ISLPED 2005: Proceedings of the 2005 international symposium on Low power electronics and design, Aug. 8-10, 2005, pp. 221-226.

Conventional PCI 2.3—An Evolution of the Conventional PCI Local Bus Specification. [online] PCI-SIG' Mar. 29, 2002 [Retrieved on Sep. 16, 2009]. Retrieved from the Internet: http://www.pcisig.com/members/downloads/specifications/conventional/conventional_pci_2_3, 328 pages.

Cringley, "Google-Mart: Sam Walton Taught Google More About How to Dominate the Internet Than Microsoft Ever Did," I, Cringely-The Pulpit, Nov. 17, 2005, 3 pages.

CUSC V1.3, "CUSC-Schedule 3," Apr. 2006, 65 pages.

Dean and Ghemawat., "MapReduce: Simplified Data Processing on Large Clusters," Communications of the ACM, Jan. 2008, 51(1):107-113.

Earley et al., "645.5 Supply Circuits and Interconnecting Cables," 2005 Ed., National Electrical Code Handbook, National Fire Protection Association, pp. 956-957.

Economou et al., "Full-System Power Analysis and Modeling for Server Environments" [online]. 2006 [Retrieved on Sep. 16, 2009]. Retrieved from the Internet: http://csl.stanford.edu/~christos/publications/2006.mantis.mobs, 8 pages.

Fan et al., "Power Provisioning for a Warehouse-sized Computer;" In Proceedings of the ACM International Symposium on Computer Architecture, Jun. 2007, 11 pages.

Felter et al., "A Performance-Conserving Approach for Reducing Peak Power Consumption in Server Systems," In ICS 2005: Proceedings of the 19th Annual International Conference on Supercomputing, Jun. 20-22, 2005, pp. 293-302.

Femal et al., "Boosting Data Center Performance Through Non-Uniform Power Allocation", IEEE Computer Society, Jun. 2005, pp. 1-12.

Femal et al., "Safe Overprovisioning: Using Power Limits to Increase Aggregate Throughput," 4th International Workshop on Power Aware Computer Systems (PACS 2004), Dec. 2004, pp. 150-164.

Filani et al., "Dynamic Data Center Power Management: Trends, Issues, and Solutions," Intel Technology Journal. Feb. 21, 2008, 12(1):59-67.

GlobeNewswire, Inc. [online] "EnerNOC Deploys Industry's First Presence-Enabled Smart Grid Technology," 2009 [retrieved on May 3, 2014]. Retrieved from the Internet: http://files.shareholder.com/downloads/ENOC/0x0x291277/4c6d4655-cfe6-4e01-a690-

ec811ee3194e/ENOC_News_2009_4_30_General_Releases. pdf , 2 pages.

Gwennap, "AppliedMicro's X-Gene: Minimizing Power in Data-Center Servers," The Linley Group, Jul. 2012, 9 pages.

Hamilton, "Commodity Data Center Design" Microsoft Corp, Apr. 17, 2007, 11 pages.

Hamilton, "An Architecture for Modular Data Centers," Microsoft Corp, CIDR 2007. 3rd Biennial Conference on Innovative Data Systems Research (CIDR), Jan. 7-10, 2007, pp. 306-313.

Heath et al., "Mercury and Freon: Temperature Emulation and Management for Server Systems," In International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS '06), Oct. 21-25, 2006, pp. 106-116.

HP Enterprise Configurator. [online]. Hewlett-Packard Development Company, 2006 [retrieved on Sep. 3, 2009]. Retrieved from the Internet: http://h30099.www3.hp.com/configurator/powercalcs.asp, 2 pages.

Intel Xeon Processor with 512-KB3 L2 Cache at 1.80 GHz to 3 GHz Datasheet. [online] Intel Corporation, Mar. 2003 [Retrieved on Sep. 24, 2009]. Retrieved from the Internet: http://download.intel.com/design/Xeon/datashts/29864206.pdf, 129 pages.

International Search Report and Written Opinion for PCT Application No. PCT/US2010/038621, mailed Feb. 18, 2011, 10 pages.

Jackson et al., "Data Center Power Requirements: Measurements from Silicon Valley," Energy 28, 2003, pp. 837-850.

Jiang et al, "Power Aware Job Scheduling with Quality of Service Guarantees: A Preliminary Study," Proceedings of the Second International Symposium on Networking and Network Security, Apr. 2010, 4 pages.

Lee, "Peeling the Power Onion," CERCS IAB Workshop, Apr. 26, 2010, 27 pages.

Matter, 'WEBLOG' [online]. "The Industrial Revolution, Finally," Retrieved from the internet: http://blogs.sun.com/Gregp/date/200610, Oct. 17, 2006, 5 pages.

Meisner and Wenisch, "Peak Power Modeling for Data Center Servers with Switched-Mode Power Supplies," ISLPED 2010, Aug. 18-20, 2010, 6 pages.

(56) References Cited

OTHER PUBLICATIONS

Moore et al., "Data Center Workload Monitoring, Analysis, and Emulation," In Eighth Workshop on Computer Architecture Evaluation using Commercial Workloads, Feb. 2005, 8 pages.

Moore et al., "Making Scheduling "Cool": Temperature-Aware Workload Placement in Data Centers," USENIX Annual Technical Conference, Apr. 10-15, 2005, pp. 61-74.

Rackable Systems, "Rackable Systems Contcentro Modular Data Center High Density, High Efficiency Mobile Computing," Datasheet 2007, 1 page.

Rackable Systems, "Thermal Management & Server Density: Critical Issues for Today's Data Center," White paper, 2004, 8 pages.

Ranganathan et al., "Ensemble-level Power Management for Dense Blade Servers," In ISCA 2006: Proceedings of the 33rd International Symposium on Computer Architecture, 2006, 12 pages.

Rasmussen, "Guidelines for Specification of Data Center Power Density," APC White Paper 120, 2005, 21 pages.

Sandoval and Soria, "A Primer on Power Factor Correction," Jun. 1, 2004, EC&M, Retrieved from the Internet: http://APrimerOnPowerFactor.mht, 5 pages.

Sanmina-Sci-Ecobay Product Sheet, "High-Performance Closed-Loop System," 2006, 2 pages.

Sawyer, "Calculating Total Power Requirements for Data Centers," White Paper 3. Revision 1, 2011, 10 pages.

Shankland, 'cNet News.com' [online]. "Sun Blackbox, meet APC's whitebox," Jan. 7, 2007, http://news.com.com/Sun+Blackbox%2C+meet+APCs+whitebox/2001-1010_3-617774.html, 2 pages.

System Management Interface Forum, "PMBus Power System Management Protocol Specification Part 1—General Requirements, Transport and Electrical Interface, Revision 1.0," Mar. 2005, 17 pages.

Terhune, "Principles for Efficient and Reliable Reactive Power Supply and Consumption," United States of America Before the Federal Energy Regulatory Commission, Mar. 8, 2005, 5 pages.

Turner IV et al., "Tier Classifications Define Site Infrastructure Performance," White Paper: The Uptime Institute, 2006, pp. 1-17.

^{*} cited by examiner

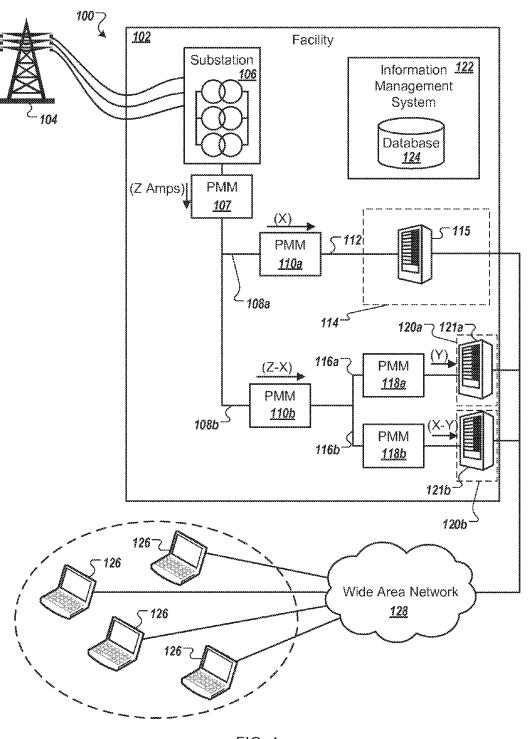


FIG. 1



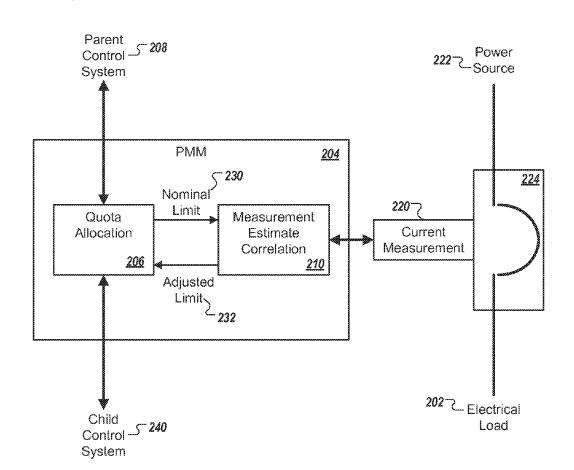


FIG. 2

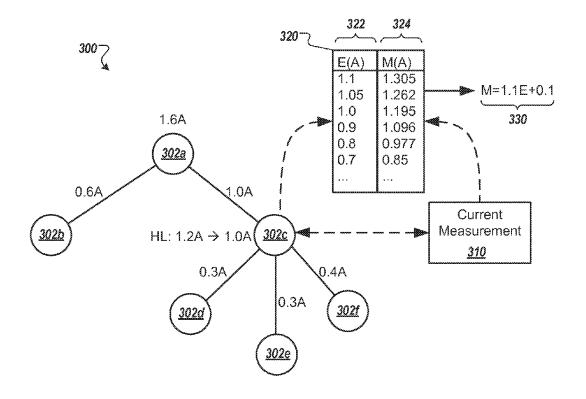


FIG. 3

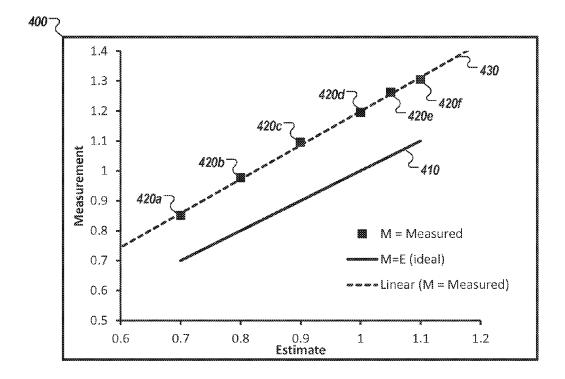


FIG. 4

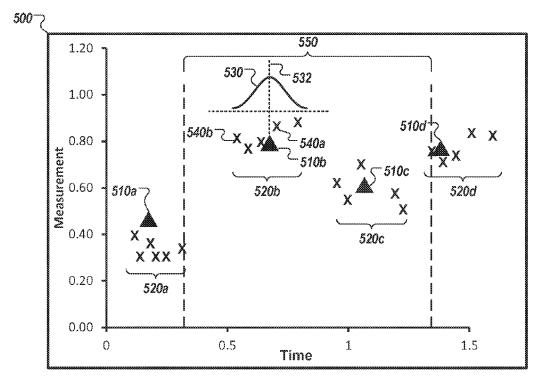
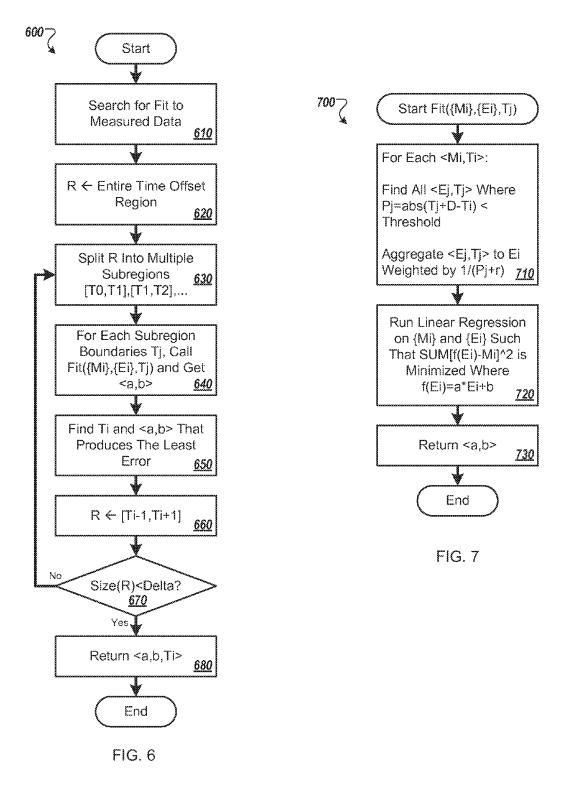
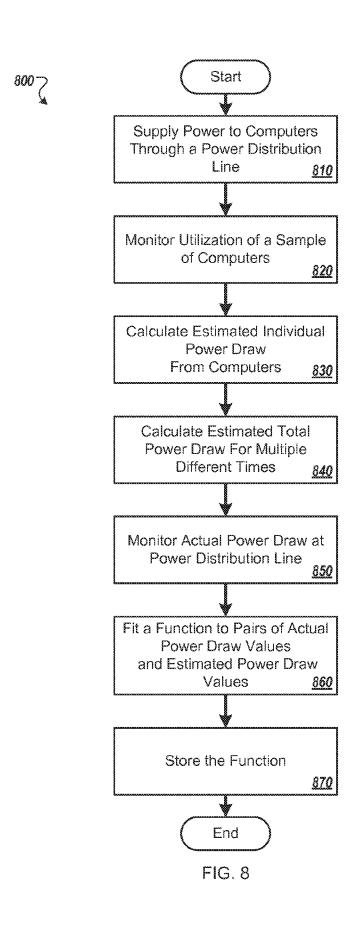
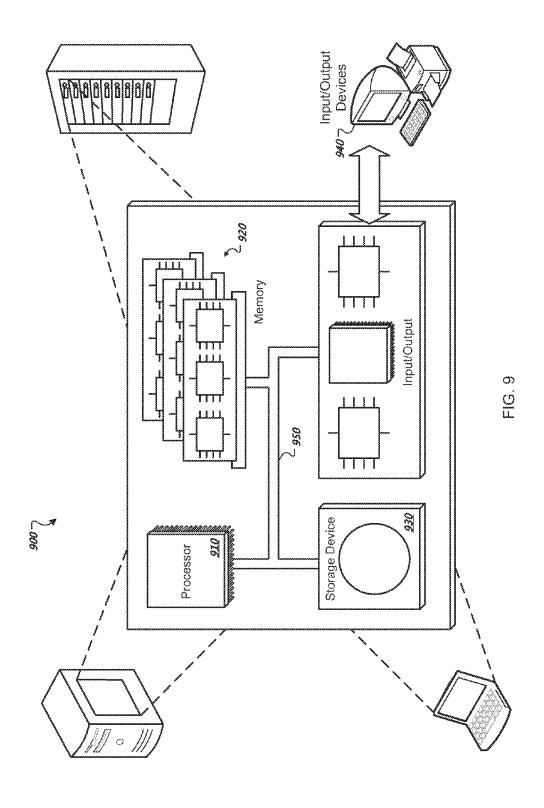


FIG. 5







ACCURATE POWER ALLOTMENT

TECHNICAL FIELD

Various implementations relate generally to electrical ⁵ power distribution.

BACKGROUND

In general, power distribution systems receive high voltage and/or high current electrical power from a utility provider, generator station, or other source of power. The power distribution systems may transform the received power to electrically powered equipment, such as the computers and cooling equipment in a data center. Electrical power is generally conducted by high current conductors that may be split into two or more branch conductors to divide and distribute electrical power. Some of these branches may be split to further divide and distribute electrical power. Each of the electrical conductors may be protected by circuit breakers, and/or other over-current protection devices to stop the flow of electrical currents in excess of the conductors' ratings.

SUMMARY

In general, this document describes systems, apparatus, and methods relate to current measurement devices that compare a load's estimated power draw to a measured power draw, and uses that feedback to improve the accuracy of subsequent power draw estimates. In an exemplary implementation, a power monitoring module (PPM) may estimate the power draw of a computer or group of computers based on a computing load assigned to the computer(s), measure the computer's actual power draw, and then use the measurement to determine a more accurate model for predicting the power draw of the computer(s) while operating at various computing loads. A more accurate model can permit a facility to operate closer to the maximum capacity of the power distribution system, with limited risk of exceeding the maximum capacity.

In a first aspect, a method of correlating power in a data 40 center comprises supplying power to a portion of a data center through a power distribution line, the portion of the data center including a plurality of computers that draw power through a circuit breaker. Utilization of at least a statistically significant sample of the plurality of computers is monitored, 45 and an estimated individual power draw for each of the sample of the plurality of computers based on the utilization is calculated. An estimated total power draw is calculated for each of a plurality of different times from the estimated individual power draw of each of the sample of the plurality of 50 computers to generate a plurality of estimated total power draw values for the plurality of different times. Actual power draw is monitored at the power distribution line by the portion of the data center and a plurality of actual power draw values is generated for the plurality of different times. A function is 55 fitted to a plurality of pairs of actual power draw values and estimated power draw values, each pair of the plurality of pairs comprising an actual draw value and an estimated draw value for the same time, and the function is then stored.

Various implementations can include some, all, or none of 60 the following features. The utilization can be CPU utilization. Calculating the estimated individual power draw can include storing data representing a computer configuration for each of the plurality of computers, and the computer configuration can be an input to a function relating utilization to estimated 65 individual power. The computer configuration can include one or more of processor speed, amount of memory or num-

2

ber of disk drives. Calculating the estimated total power draw can include summing the estimated individual power draw of each of the sample of the plurality of computers. The sample can include substantially all of the plurality of computers. The function can be a linear function. The method can also include receiving a maximum power draw value for the portion of the data center, calculating an estimated current individual power draw value for each of the sample of the plurality of computers based on the utilization, calculating an estimated current total power draw value from the estimated current individual power draw value of each of the sample of the plurality of computers, at least one of calculating an adjusted maximum power draw value from the maximum power draw value and the function, and comparing the estimated current total power draw value to the adjusted maximum power draw value, or calculating an adjusted estimated current total power draw value from the estimated current total power draw value and the function, and comparing the adjusted estimated total power draw value to the maximum power draw value, and adjusting operation of at least one computer in the plurality of computers based on the comparison. Adjusting operation can include one or more of adjusting job allocation, adjusting job scheduling or adjusting central processing unit execution frequency. Calculating an estimated individual power draw for a computer of the plurality of computers can include measuring utilization of the computer at a plurality of utilization measurement times to generate a plurality of utilization values for the computer, there being a greater number of utilization measurement times than different times such that there are a greater number of utilization values for the computer than different times. The method can further include associating each of the plurality of utilization values with one of the plurality of different times. Associating can include comparing a utilization measurement time for a utilization value with a midpoint between two adjacent different times. There can be a plurality of utilization value times for each of plurality of different times. Calculating the estimated total power draw for one of the different times can include weighting each one in a collection of estimated values based on a difference between a value estimating time for the estimate value and the different time. The method can further include synchronizing time stamps of the different times and the utilization measurement times. Calculating an estimated total power draw can occur at a higher frequency than monitoring actual power draw. The adjusting operation may include adjusting a multiplicity of computers in the plurality of computers to cause the estimated current total power draw value to be closer to the adjusted maximum power draw value. The adjusting operation may include adjusting a multiplicity of computers in the plurality of computers to cause the adjusted estimated total power draw value to be closer to the maximum power draw value.

The systems and techniques described here may provide one or more of the following advantages. First, a system can provide a more accurate estimate of the power to be used in a facility such as a data center. Second, a system can operate closer to the maxim capacity of the power distribution system. Third, the risk of exceeding the maximum capacity can be maintained at an acceptable level. Fourth, the system can help capture deployment or accounting errors if failed or highly skewed correlation is observed.

The details of one or more implementations are set forth in the accompanying drawings and the description below. Other features and advantages will be apparent from the description and drawings, and from the claims.

DESCRIPTION OF DRAWINGS

FIG. 1 shows an example power distribution system for the dynamic estimation of power consumption of power nodes.

FIG. 2 is a block diagram that shows an example of a system for estimating the power draw of an electrical load.

FIG. 3 depicts an example peer-to-peer interaction among intelligent protection modules for the dynamic estimation of power draw of power nodes.

FIG. 4 is a graph of example estimated power consumption and example measured power consumption.

FIG. 5 is another graph of example estimated power consumption and example measured power consumption.

FIGS. **6-8** are flow diagrams of an example processes for ¹⁰ estimating the power consumption of an electrical load.

FIG. 9 is a schematic diagram of an example of a generic computer system.

Like reference symbols in the various drawings indicate like elements.

DETAILED DESCRIPTION

This document describes systems and techniques for accurately estimating the power to be used in a facility such as a 20 data center. Electrical devices are generally rated for a maximum current draw, and in some instances these ratings can be somewhat conservative. In addition, the electrical devices may only occasionally, if ever, draw their rated currents. In some instances, power distribution systems can be conserva- 25 tively built to supply the devices' rated currents. The collective power of the devices connected to branches of the power distribution system may remain conservatively below the breaker limit for their respective branch, and the attached devices may not be drawing their maximum amount of power 30 simultaneously. Overall, a power distribution system may leave some portion of the available power unused, and the amount of unused power may increase as the number of power branches increases.

Hypothetically, the power draw of a computer can be estimated from the utilization of the computer. However, the algorithm that computes power draw from utilization may not be accurate. In addition, even if the computation of the power draw from a single computer is accurate, summation of the power draw of multiple computers may not accurately represent power drawn from a branch line, e.g., due to the presence of other devices on the branch line such as routers, lighting, and cooling systems. However, a load's estimated power draw can be compared to a measured power draw, and that feedback can be used to improve the accuracy of subsequent 45 power draw estimates.

FIG. 1 shows an example power distribution system 100 that includes dynamic estimation of power consumed by nodes in the system 100. The estimations for each node are configured to achieve improved utilization of electrical distribution infrastructure by allowing a network of current protection devices to intelligently estimate power draws of electrical loads in response to dynamic load conditions. For example, in the example depicted in FIG. 1, the power distribution system 100 includes a number of such power monitors, 55 or power monitoring modules (PMMs), that estimate and monitor the amount of power needed by various load circuits under variable load conditions, while protecting the source node against overloads that would exceed the source node's predetermined capacity limit. For example, when current 60 through a PMM approaches full (e.g., 100%, greater than 90%, greater than 85%) utilization of its present capacity estimation, the PMM may generate a request message asking child nodes to limit or reduce their present power consumption. In some implementations, the request message may include a workload limit for the child node. For example, the child node may be an electrical load such as a computer with

4

a controllable clock speed, in which the speed of the computer's clock has a corresponding effect on the amount of power consumed by the computer. In another example, the child node may be an electrical load such as a battery charger or ventilation fan, in which the charge rate or fan speed, respectively, have corresponding effects on the amount of power drawn by the child node. Accordingly, a number of PMMs can operate together to automatically negotiate a capacity sharing arrangement that adapts to the dynamic load conditions to achieve improved utilization of infrastructure power handling capability to distribute power to meet peak power demands at different loads.

The power distribution system 100 includes a facility 102 that receives high voltage and/or current power from an electrical utility provider 104. The facility 102 can include a power substation 106. The power substation 106 transforms the high voltage and/or current power into usable voltages and/or currents for electrical loads in the facility 102, and distributes the transformed power through a power monitoring module (PMM) 107, and on to branch conductor 108a and a branch conductor 108b.

The branch conductor 108a includes a PMM 110a, and the branch conductor 108b includes a PMM 110b. The PMM 110a provides power monitoring for a circuit 112 that supplies power to an electrical load 114. The PMM 110b provides power monitoring for a branch conductor 116a and a branch conductor 116b. The branch conductors 116a and 116b include a PMM 118a and a PMM 118b, respectively. Although only two branches are shown at each level of the hierarchy of the power distribution system 100, this is merely illustrative, and any given node could have three or more branches, and there could be additional levels to the hierarchy.

The PMMs 118a and 118b provide power monitoring for an electrical load 120a and an electrical load 120b, respectively. If the facility is a data center, then the electrical loads 114, 120a, 120b include at least some server computers 115, 121a, 121b, respectively, such as one or more racks of server computers. In some implementations, the electrical loads 114, 120a, and 120b can include computers, collections of computers, racks of server computers, collections of racks of server computers, networking equipment, environmental control equipment, lighting systems, or combinations of these and other appropriate forms of electrical loads.

The PMMs 107, 110a, 110b, 118a, and 118b, are able to estimate and measure the amount of power consumed by their respective child nodes, and can communicate with each other to allocate power from a shared supply. In some implementations, the PMMs 107, 110a, 110b, 118a, and 118b can communicate in a peer-to-peer network. For example, the PMM 110a may send a message to the PMM 107 to request that the PMM 110b reduce the amount of power being consumed by its child nodes. If the request is granted, the PMM 110a may then raise the amount of power available to its own child nodes by a substantially like amount. In some implementations, the PMMs 107, 110a, 110b, 118a, and 118b can communicate with an information management system 122 which includes a database 124. For example, the PMMs may communicate with the information management system 122 to request and/or receive power estimation settings, or to send and/or receive statuses, alarms, notifications, configurations, or other data that may be used by the PMMs 107, 110a, 110b, 118a, and 118b. In some implementations, the information management system 122 can access the database 124 to store and retrieve information relating to the PMMs 107, 110a, 110b, 118a, and 118b.

The server computers 115, 121a, and 121b, serve information to a number of computers 126 via a wide area network (WAN) 128. In some implementations, the computers 126 can place varying computing loads upon the server computers 115, 121a, and 121b. For example, the server computer 115 may host email services, and the server computers 121a and 121b may host video sharing services. Demand for these two different services can vary as the amount of traffic from the computers 126 varies. For example, demand for email services may increase in the daytime as users of the computers 126 access their email for work, but in the evening the demand for email services may decrease while the demand for video sharing services increases as people browse videos during their free time.

As the computing loads vary, electrical current needs of the electrical loads **114**, **120***a*, and **120***b* can vary as well. For example, during the day the computing load placed upon the server computers **115** may cause the electrical load **114** to draw 60 A of electrical power in order to operate, while the electrical loads **120***a* and **120***b* draw 20 A. At night, the server computers **115** may experience lower computing load, and therefore the electrical load **114** may draw 40 A while the server computers **121***a* and **121***b* may experience increased computing loads such that the electrical loads **120***a* and **120***b* experience a 70 A draw.

The branch conductors **108***a* and **108***b* share a supply, illustrated as "Z" amperes. The power monitoring module **110***a* is configured to allow a number of amperes of current, designated as "X", to pass onto the conductor **112**, and the power monitoring module **110***b* is configured to allow the 30 remaining number of amperes of current, designated by the value "Z-X", to pass along the conductors **116***a* and **116***b*. As the electrical current demand of the electrical load **114** varies, the value of "X" can vary as well. For example, Z may be 100 A, and the load connected to the PMM **110***a* may draw 35 approximately 75 A, leaving approximately 25 A of capacity available for use by the loads connected to PMM **110***b* without exceeding the 100 A supply.

The PMM **110***a* may limit the amount of power it passes by estimating the amount of power the electrical load **114** consumes at a given utilization of the server computers **115**. The PMM **110***a* may determine that the electrical load **114** will consume approximately "X" amperes when the server computers **115** are utilizing N % of their computing capacity. The PMM may request the server computers **115** in the electrical 45 load **114** to limit their combined computing load to N % of capacity, thereby limiting the combined power consumption of the electrical load **114** to approximately "X" amperes.

The estimation, however, may not be completely accurate. For example, the electrical load 114 may actually draw 0.8*X 50 amperes when the server computers 115 are at N % of computing capacity, thus underutilizing the amount of power available to the electrical load 114 (e.g., the server computers 115 could operate at more than N % utilization when the electrical load 114 is using "X" amperes), or the electrical load 114 may actually draw 1.3*X amperes when the server computers 115 are operating at N % utilization, thus consuming more than the "X" amperes allotted to the PMM 110a (e.g., and trip an "X" ampere circuit breaker, and over-stress the branch conductor 108a or the substation 106).

The PMMs 107, 110a, 110b, 118a, and 118b estimate the amounts of power their respective child nodes will draw for their respective utilizations, and compare those estimates to respective measured amounts of power that are drawn by their respective child nodes as they operate at those configured 65 utilizations. The PMMs 107, 110a, 110b, 118a, and 118b compare the estimated and measured power amounts to deter-

6

mine correlations between utilization levels and actual power consumption. The PMMs 107, 110a, 110b, 118a, and 118b may then use the determined correlations when performing subsequent estimations of the power draws for various corresponding utilizations.

In some implementations, by intelligently allocating capacity among the PMMs 107, 110a, 110b, 118a, and/or 118b, electrical power utilization can increase without increasing the electrical supply capacity. For example, PMMs 107, 110a and 110b can be initially allocated 50 A each while the electrical load 114 is drawing 20 A, the electrical loads 120a and 120b are drawing 20 A each, and the PMMs 118a and 118b may be allocated 25 A apiece. The PMM 110a has approximately 30 A of excess capacity (50 A-20 A=30 A), while the PMM 110b may have $10 \text{ A} (50 \text{ A} - (2 \times 20 \text{ A}) = 10 \text{ A})$. As computing demands change, the electrical load 120a may need to be reconfigured to draw an estimated 40 A, exceeding the allocation given to the PMM 118a. In some implementations, the PMM 118a can request the PMM 118b to limit its consumption by 5 A. If granted, the PMM 118b can reduce its estimated power consumption to 20 A and the PMM 118a can increase its own estimated power consumption to 30 A.

In this example, the PMMs 118a and 118b have substantially maximized their use of the 50 A allocated to the PMM 110b. However, there remains a 10 A shortage along the branch conductor 116a. In some implementations, the PMM 110a can request an additional power allocation from the upstream PMM 110b. For example, the PMM 118a can request an additional 10 A allocation from the PMM 110b. However, in this example, the PMM 110b is already configured to consume its allocated 50 A. In some implementations, the PMM 110b can send a message to the PMM 110a to determine if the PMM 110a has any unused capacity that could be re-allocated to the PMM 110b.

For example, the PMM 110b may request a 10 A allocation from the PMM 110a. Since the PMM 110a has 30 A of excess capacity, the PMM 110a may lower its own estimated power usage by 10 A and grant the freed capacity to the PMM 110b. The PMM 110b can then raise its estimated power use by 10 A to a total of 60 A, thereby satisfying the power needs of the electrical loads 120a and 120b, and increase the utilization of the 100 A available from the substation 106. Additional examples of intelligent power estimation are discussed in further detail in relation to FIGS. 2-8.

FIG. 2 is a block diagram that shows an example of a system 200 for estimating the power draw of an electrical load 202. The system 200 includes a power monitoring module (PMM) 204. In some implementations, the PMM 204 can be the PMM 107, 110a, 110b, 118a, or 118b of FIG. 1.

The PMM 204 includes a quota allocation module 206. The quota allocation module 206 is communicatively connected to a parent control system 208. The quota allocation module 206 communicates with the parent control system 208 to receive power allocation quotas. For example, the parent control system 208 may determine that the PMM 204 is to be granted a 50 A allocation of power and communicate information about that allocation to the quota allocation module 206. In reference to FIG. 1, the PMM 118a may receive a power allocation of "Y" amperes from its parent PMM 110b.

Referring back to FIG. 2, the quota allocation module 206 is communicatively connected to a measurement estimation correlation module 210. The measurement estimate correlation module 210 is also communicatively connected to a current measurement module 220. The current measurement module 220 measures the amount of power flowing from a power source 222 to the electrical load 202 through a circuit protection module 224 (e.g., a circuit breaker). The measure-

ment estimate correlation module 210 communicates with the current measurement module 220 to receive information descriptive of the amount of power being consumed by the electrical load 202.

The measurement estimate correlation module 210 5 receives a nominal limit value 230 from the quota allocation module 206, and determines an adjusted limit value 232 that is provided back to the quota allocation module 206. The quota allocation module 206 then provides the adjusted limit value to a child control system 240 associated with the elec- 10 trical load 202. In some implementations, the adjusted limit value may also be provided to the parent control system 208. For example, the parent control system 208 may use the adjusted limit value to improve the accuracy of power allotments. The adjusted limit value is a value that the measurement estimate correlation module 210 estimates the child control system 240 will consume when operating in accordance with the adjusted limit value 232. In some implementations, the child control system 240 may be an electrical load, such as a computer or collection of computers, e.g., the server 20 computers 115. For example, the adjusted limit value 232 may be a utilization value that may limit or otherwise control the computing throughput of the server computers 121a to control the amount of power consumed by the electrical load 120a. The PMM 118a may transmit the adjusted limit value 25 a corrected estimated power draw M that is in the same 232 to the server computers 121a to cause the electrical load 120a to consume an estimated "Y" amperes.

In some implementations, the child control system 240 may be another PMM. For example, the adjusted limit value 232 may be an amperage limit that the downstream PMM is 30 being requested to honor.

The current measurement module 220 measures the actual amount of power being consumed by the electrical load 202. As such, the measurement estimate correlation module 210 compares the nominal limit value 230, the adjusted limit 35 value 232, and the actual power consumption measurement provided by the current measurement module 220 to modify the estimated correlation between the nominal limit value 230 and adjusted limits to determine a new value for the adjusted limit value 232. The new value for the adjusted limit value 232 40 is then provided by the quota allocation module 206 to the child control system 240. In some implementations, by using the measured power consumptions of the electrical load at various adjusted limits, the PMM 204 may determine improved adjusted limits that cause the electrical load 202 to 45 consume power at levels that better adhere to respective nominal limits.

FIG. 3 depicts an example peer-to-peer interaction 300 among a hierarchy of power monitoring modules (PMMs) 302a-302f for the dynamic estimation of power draw of 50 power nodes. In some implementations, the PMMs 302a-**302***f* can be the PMM **204** of FIG. **2**, or the PMMs **107**, **110***a*, **110***b*, **118***a*, or **118***b* o FIG. **1**.

When deciding how to set the utilization "U" of the computers in order to achieve a target power "P", in some imple- 55 mentations one technique would be to use some function U=f(P). In some implementations, each computer receives the target power P from a PMM, and calculates its utilization from U=f(P) using the function f for that computer. The function f can be different for different computers, e.g., the 60 function f can depend on the components in the computer. In some implementations, function f can be described in a lookup table, or by a collection of mathematical formulae in which each formula estimates power consumption for a corresponding utilization subrange.

The problem is that, in some implementations, the function f(P) may not be accurate due to limited measurement samples

and workload varieties. In addition, the quota P given to a computer may not accurately represent the real available capacity due to the presence of other devices on the branch line such as routers, lighting, and cooling systems. For example, as previously noted, the function f(P) may not accurately represent power drawn from a branch line, e.g., due to the presence of other devices on the branch line such as routers, lighting, and cooling systems.

To reduce this inaccuracy, actual capacity, often defined by the circuit breaker's hard limit (HL0) of the branch, can be converted to an adjusted capacity (HL1) which is distributable to computers. In some implementations, a dynamic conversion of actual capacity to adjusted capacity can be performed in a three step process. An estimated power draw is calculated from utilization values. A functional relationship between the estimated power draw and the measured power draw is determined. The adjusted capacity is calculated based on the determined function using the actual capacity as the

In some implementations, the actual capacity can be converted to adjusted capacity using a function HL0=g(HL1), where g(HL1)=a*HL1+b. The function g can also be considered as a function that converts an estimated power draw E to parameter space as the measured power draw, e.g., M=g(E)=a*E+b. Parameter "a" generally compensates for proportional error between measured power draw and respective utilization-derived estimated power draw. Parameter "b" generally represents the constant power consumed by equipment but not included in a given aggregation path (e.g. networking equipment, lighting). The parameters "a" and "b" can be periodically updated to adjust for variations in the electrical loads 114, 120a, 120b.

In some implementations, parameters "a" and "b" can be determined from aggregated power estimates from the server computers and power drawn by the loads. The power drawn by the load is measured by the PMM at various different times, to create a plurality of measured power values "M". Similarly, the estimated power drawn by the server computers is collected at various different times to create a plurality of measured utilization values "E". For example, each of a statistically significant number of the server computers, e.g., all of the server computers, can calculate an estimated power Q drawn by that computer based on the utilization V at that computer, e.g., using the function f with V=f(Q). Each of these computers can report their estimated power draw O to the PMM, and the PMM aggregates, e.g., sums, the estimated power Q from the individual server computers to generate an estimated power draw E' for the server computers in the load.

At least one estimated power value "E" is associated with each of at least some of the plurality of measured power values "M", thereby generating a sequence of pairs of measured power values "M" and estimated power values "E". For example, a collection of servers may each estimate their own individual power values on a substantially periodic interval (e.g., every 0.1, 1, 5, 10, 30, 60 seconds) and provide those estimated power values V to the PMM from which the servers draw their power. The PMM may then combine those individual estimated values to determine the estimated power value "E", and compare that estimate to power values M' that are measured by the PMM relatively less frequently (e.g., every 0.25, 0.5, 1, 2, 10 minutes). Due to potential higher sampling rate of the estimated power values E', multiple values of E' can be combined, e.g., averaged, to provide a single estimated power value E' for a given measured power value M'.

The values "a" and "b" can be calculated by fitting a function, e.g., a linear function, to the pairs of measured power values "M" and estimated power values "E". In essence, the pairs of values (M', E') provide a scatter plot to which the function is fit.

The fitting can generate a function g(x)=a*x+b. When used as a function to convert an estimated power draw E to a corrected estimated power draw M in the parameter space of the measured power draw, the functional relationship between the estimated power draw "E" and the corrected 10 estimated power draw "M" can be a linear function, e.g., using the slope-intercept equation:

M=aE+b

Similarly, when considered as a function to convert to 15 actual capacity HL0 to adjusted capacity HL1, the functional relationship can be a linear function:

HL0=a*HL1+b

In either case, parameter "a" compensates for proportional 20 error, and parameter "b" represents the constant power consumed by equipment plugged into the power hierarchy but not included in a given aggregation path, such as networking equipment, lighting, and/or other appropriate electrical loads that are substantially constant contributors to a measured 25 aggregate electrical load.

At levels where there is power measuring equipment such as a current measurement module 310, a collection of aggregated model-based estimates (E') 322 is compared with a collection of actual readings (M') 324 to determine values for 30 the correlation value "b" and the proportional error value "a". In the illustrated example, the PMM 302c has gathered a collection of samples 320 during a recent time window by both aggregating updates from its child nodes (e.g., PMMs 302d-302f), and by communicating with the current measurement module 310.

In the illustrated example, fitting of a linear function 330 to the data generates an equation M=1.1E+0.1, i.e., the proportional error value "a"="1.1" and the correlation value "b"="0.1". As such, when the PMM 302c determines the 40 adjusted actual through which the 1.2 actual limit is converted to a 1.0 actual hard limit because the equation 330 projects that an estimated usage of 1.0 can lead to 1.2 actual measured power consumption. In use, the current measurement module 310 may not be able to provide readings as fine-grained or 45 time-synchronized as the model-based estimates. In some implementations, multiple estimates may be aggregated to correlate one or more measurement readings. In some implementations, the fitting may be determined such that the model substantially compensates for any time offset between the 2 50 time series. A graphical example of a linear model of estimated values fitted to a collection of measured values is discussed in the description of FIG. 4.

FIG. 4 is a graph 400 of example estimated power consumption and example measured power consumption. The 55 graph 400 includes a line 410 which represents the ideal case, where measured values "M" are equal to estimated values "E". The graph 400 also includes a collection of measured values 420. The collection of measured values 420a-420f are measured values "M" for power consumption at selected 60 estimated values "E" for power consumption. For example, for a predetermined level of utilization of the server computers 115, the estimated value "E" of the electrical load's 114 power consumption may be "1.0", however, the actual measured value "M" 420c may be "1.195".

A function, e.g., a linear function, represented by a line **430**, is fit to the collection of measured values **420***a***-420***c*. A

10

higher-order polynomial function can be used if a linear function does not fit sufficiently well. In some implementations, the determined solution represented by the line 430 may be used by the PMMs 110b and 302c to estimate the allocatable power capacity for the given actual limit. In some implementations, the determined solution represented by the line 430 may be used by the PMMs 107, 110a, 110b, 118a, 118b, 204, and 302a-302f to estimate the actual power consumption of various electrical loads for given estimated power values. In some implementations, PMMs 107, 110a, 110b, 118a, 118b, 204, and 302a-302f may use the determined solution as part of a process to determine utilization and/or other appropriate settings that may cause various electrical loads to consume a predetermined amount of electrical power.

FIG. 5 is another graph 500 of example estimated power consumption and example measured power consumption over time. The graph 500 includes a sequence of measured power consumption values 510a-510d. The graph 500 also includes a sequence including a collection of estimated power values 520a, a collection of estimated power values 520b, a collection of estimated power values 520c, and a collection of estimated power values 520d. In some implementations, the measured values 510a-510d, and the values in the collections of estimated values 520a-520d may include timestamps of when the different estimations and measurements were taken.

Each of the collections of estimated values 520a-520d represents multiple estimated power consumption values taken at various times for a corresponding one of the measured power consumption values 510a-510d. In some implementations, estimated power consumption values may be taken multiple times because the estimates may often have higher frequency than then measurements.

In some implementations, a single measured value may be determined for each of the collections of measured values 520*a*-520*d*. In some implementations, an average value may be determined for each of the collections 520*a*-520*d*.

In some implementations, each of the collections of estimated values 520a-520d may be associated with one of a collection of different times. In the illustrated example, each of the collections of estimated values 520a-520d is associated with corresponding measured values 510a-510d, taken at different times.

In some implementations, a weighting process may be applied to determine a value to represent each of the collections of estimated values 520a-520d. For example, current consumption values that have been estimated at points in time close to when a measured midpoint value has been assigned may be given relatively greater mathematical weight than are values estimated at relatively more distant periods of time away from the measured value. In the illustrated example, a statistical normal curve 530 has been superimposed on the graph 500 and centered on a midpoint 532 when the measured value 510b was assigned to an electrical load. The curve 530represents one example type of weighting that may be used to determine the relative influences of the various values included in the collection 520b (e.g., vertically higher points on the curve 530 represent relatively higher weightings for estimated values than points vertically lower on the curve 530). In the illustrated example, an estimated value 540a is given a statistically greater weight than an estimated value 540b because the value 540a is time-wise closer to the measured value **510***b* than the value **540***b*.

In some implementations, solutions for estimated power consumption values may be fitted to substantially only the measured power consumption values measured within a predetermined time period. For example, by limiting the fitting process to only consider values measured within a recent

window of time, changes in the offset value "b" may be better accommodated (e.g., a cooling fan or light was turned on or off, a network switch was added to or removed from the measured electrical load). In the illustrated example, a time period 550 includes the estimated values 510b and 510c and 5 the collections of measured values 520b and 520c, and as such, a model may be determined those values while ignoring values from beyond the time period 550 (e.g., values 510a and 510d, collections 520a and 520d).

FIGS. **6-8** are flow diagrams of example processes for 10 estimating the power consumption of an electrical load. Referring to FIG. **6**, an example process **600** starts at step **610**. In some implementations, the process **600** may be performed by the PMMs **107**, **110a**, **110b**, **118a**, **118b**, **204**, and **302a**-**302f** of FIGS. **1-3**. At step **610**, a mathematical function that 15 fits a collection of measured data is sought. At step **620**, a region "R" is determined as an entire time offset region. At step **630**, the region "R" is split into multiple subregions (e.g., T0 to T1, T1 to T2, . . .).

At step **640**, for each subregion boundary, designated as 20 "Tj", a fitting process is performed for a collection of measured power consumption values "Mi" and a collection of estimated power consumption values "Ei" determined at the corresponding subregion boundary "Tj" to determine the proportional error value "a" and the offset value "b". An example 25 fitting process is discussed in the description of FIG. **7**.

At step **650**, a time "Ti" and the corresponding values of "a" and "b" that produce substantially the least error at time "Ti" are found. At step **660**, the region "R" is designated as extending from one time period prior to the time "Ti" to one 30 time period after the time "Ti".

At step 670, if the size of the region "R" is determined not to be less than a predetermined size "Delta", then step 630 is repeated. If, however, at step 670 the size of the region "R" is determined to be less than a predetermined size "Delta", then 35 at step 680 the values for "a", "b", and "Ti" are returned.

Referring now to FIG. 7, an example process 700 is illustrated. In some implementations, the process 700 may be performed by the PMMs 107, 110a, 110b, 118a, 118b, 204, and 302a-302f of FIGS. 1-3. In some implementations, the 40 process 700 may be the fitting process performed as part of step 640 of FIG. 6. The process 700 starts with a given collection of measured power consumption values "Mi", a collection of estimated power consumption values "Ei", and a time boundary value "Tj".

At step **710**, for each measured value "Mi" and corresponding time "Ti", all corresponding pairs of "Ej" and "Tj" are found for when "Pj", which equals the absolute value of "(Tj+D-Ti)," is less than a predetermined threshold value. "Ej" and "Tj" are aggregated to "Ei", weighted by the mathematical function "1/(Pj+r)".

At step 720, linear regression is performed on the collection of measured values "Mi" and the collection of expected values "Ei" such that the SUM(f(Ei)-Mi)^2 is substantially minimized where f(Ei)=a*Ei+b. At step 730, the values of "a" 55 and "b" are returned.

Referring now to FIG. **8**, an example process **800** is illustrated. In some implementations, the process **800** may be performed by the PMMs **107**, **110***a*, **110***b*, **118***a*, **118***b*, **204**, and **302***a*-**302***f* of FIGS. **1-3**. In general, the process **800** 60 describes a technique for comparing estimated power draws and corresponding measured power draws to determine a function that more accurately estimates power draws.

Power is supplied to computers through a power distribution line (step **810**). For example, the PMM **110***a* can receive 65 "X" amperes through the branch conductor **108***a*. The utilization of a sample of computers is monitored (step **820**). For

example, the server computers 115 in the electrical load 114 may be monitored to determine their utilization (e.g., percent of used computing capacity).

12

In some implementations, the estimated individual power draw from the computers in an electrical load can be calculated based on the individual utilization of each computer. For example, the electrical load 114 may include fifteen computers drawing an estimated total of 6 A. In such an example, each computer in the electrical load 114 may be drawing a calculated average of $\frac{6}{15}$ of an amp, or 0.4 A per computer.

At step **840**, the estimated total power draw is calculated at multiple different times, e.g., corresponding to different times at which utilization measurements were collected.

As the utilization is monitored, the actual power draw at the power distribution line is also monitored (step 850). For example, the PMM 204 may use the current measurement module 220 to measure the actual amount of power being passed from the electrical power source 222 to the electrical load 202. Each estimated total power draw value is associated with an actual power draw value based on the time of the measurement, e.g., as discussed for FIG. 5.

A function is fitted to pairs of actual power draw values and estimated power draw values (step 860). In some implementations, the actual power draw values and estimated power draw values are paired time-wise. For example, the measured value 510b may be paired with the estimated value 540a because the estimated value 540a is the estimated value that is the closest, time-wise of the collection of estimated values **520***b* to the measured value **510***b*. In some implementations, the measured power draw values may be paired with aggregate values that represents collections of time-wise close estimated values. For example, the measured value 510a may be paired with a mean value of the collection of estimated values 520a. In some implementations, the PMM 110a may perform the fitting processes 600 or 700 to determine the fit function represented by the line 430 of FIG. 4. In some implementations, the function can be a linear function. In the example of the line 430, the fit function is determined by a comparison of measured values "M" to estimated values "E", which generates a proportional error parameter "a" and an offset "b" as applied to the formula "M=aE+b". In some implementations, calculating the estimated individual power draw can include storing data representing a computer configuration for each of a collection of computers, and the computer configuration can be an input to a function relating utilization to estimated individual power. In some implementations, the computer configuration can include one or more of processor speed, amount of memory or number of disk drives. For example, the PMM 110a may estimate that the electrical load 114 will draw an estimated 22 A of power at 65% utilization.

In some implementations, calculating the estimated total power draw can include summing the estimated individual power draw of each of the sample of the plurality of computers. In some implementations, the sample can include substantially all of the plurality of computers. For example the electrical load **114** may include twenty computers that are estimated to draw approximately 1.5 A each at 90% utilization. The PMM **110***a* may therefore estimate that the electrical load **114** may draw approximately 1.5×20, or 30 A.

At step 870, the function is stored. For example, the PMM 110a may store the function and then recall and use the function again at a later time, such as to determine an updated estimated power draw for an updated utilization level.

In some implementations, the process **800** may also include steps for receiving a maximum power draw value for the portion of the data center, calculating an estimated current

individual power draw value for each of the sample of the plurality of computers based on the utilization, and calculating an estimated current total power draw value from the estimated current individual power draw value of each of the sample of the plurality of computers. In some implementations, the process 800 can also include steps for calculating an adjusted maximum power draw value from the maximum power draw value and the function, and comparing the estimated current total power draw value to the adjusted maximum power draw value. In some implementations, the process 800 can also include steps for calculating an adjusted estimated current total power draw value from the estimated current total power draw value and the function, and comparing the adjusted estimated total power draw value to the 15 maximum power draw value. In some implementations, the process 800 can also include a step for adjusting operation of at least one computer in the plurality of computers based on the comparison. For example, the PMM 110a may receive an allocation of "X" amperes that can be drawn from the substa- 20 tion 106. To substantially maximize the usage of the allocated power, the PMM 110a may determine a utilization level for the electrical load 114 that may cause the electrical load 114 to draw an estimated "X" amperes of current.

In some implementations, the adjusting operation may include one or more of adjusting job allocation, adjusting job scheduling or adjusting central processing unit execution frequency. For example, the PMM **110***a* may alter the power draw of the electrical load **114** by altering the number of jobs assigned to the electrical load's **114** computers, by altering the scheduling of when computing jobs are to be performed (e.g., load leveling), or by altering the speed of the computers' CPUs.

FIG. 9 is a schematic diagram of an example of a generic computer system 900. The system 900 can be used for the operations described in association with the processes 600-800 according to some implementations. For example, the system 900 may be included in either or all of the PMMs 107, 110a, 110b, 118a, 118b, 204, 302a-302f, and/or the computers 115, 121a, and 121b in the electrical loads 114, 120a, and 120b

The system 900 includes a processor 910, a memory 920, a storage device 930, and an input/output device 940. Each of the components 910, 920, 930, and 940 are interconnected 45 using a system bus 950. The processor 910 is capable of processing instructions for execution within the system 900. In one implementation, the processor 910 is a single-threaded processor. In another implementation, the processor 910 is a multi-threaded processor. The processor 910 is capable of 50 processing instructions stored in the memory 920 or on the storage device 930 to display graphical information for a user interface on the input/output device 940.

The memory 920 stores information within the system 900. In one implementation, the memory 920 is a computer-read- 55 able medium. In one implementation, the memory 920 is a volatile memory unit. In another implementation, the memory 920 is a non-volatile memory unit.

The storage device 930 is capable of providing mass storage for the system 900. In one implementation, the storage 60 device 930 is a computer-readable medium. In various different implementations, the storage device 930 may be a floppy disk device, a hard disk device, an optical disk device, or a tape device.

The input/output device **940** provides input/output operations for the system **900**. In one implementation, the input/output device **940** includes a keyboard and/or pointing

14

device. In another implementation, the input/output device 940 includes a display unit for displaying graphical user interfaces.

The features described can be implemented in digital electronic circuitry, or in computer hardware, firmware, software, or in combinations of them. The apparatus can be implemented in a computer program product tangibly embodied in an information carrier, e.g., in a machine-readable storage device or in a propagated signal, for execution by a programmable processor; and method steps can be performed by a programmable processor executing a program of instructions to perform functions of the described implementations by operating on input data and generating output. The described features can be implemented advantageously in one or more computer programs that are executable on a programmable system including at least one programmable processor coupled to receive data and instructions from, and to transmit data and instructions to, a data storage system, at least one input device, and at least one output device. A computer program is a set of instructions that can be used, directly or indirectly, in a computer to perform a certain activity or bring about a certain result. A computer program can be written in any form of programming language, including compiled or interpreted languages, and it can be deployed in any form, including as a stand-alone program or as a module, component, subroutine, or other unit suitable for use in a computing environment.

Suitable processors for the execution of a program of instructions include, by way of example, both general and special purpose microprocessors, and the sole processor or one of multiple processors of any kind of computer. Generally, a processor will receive instructions and data from a read-only memory or a random access memory or both. Typical elements of a computer may include a processor for executing instructions and one or more memories for storing instructions and data. Generally, a computer will also include, or be operatively coupled to communicate with, one or more mass storage devices for storing data files; such devices include magnetic disks, such as internal hard disks and removable disks; magneto-optical disks; and optical disks. Storage devices suitable for tangibly embodying computer program instructions and data include all forms of non-volatile memory, including by way of example semiconductor memory devices, such as EPROM, EEPROM, and flash memory devices; magnetic disks such as internal hard disks and removable disks; magneto-optical disks; and CD-ROM and DVD-ROM disks. The processor and the memory can be supplemented by, or incorporated in, ASICs (applicationspecific integrated circuits).

To provide for interaction with a user, the features can be implemented on a computer having a display device such as a CRT (cathode ray tube) or LCD (liquid crystal display) monitor for displaying information to the user and a keyboard and a pointing device such as a mouse or a trackball by which the user can provide input to the computer.

The features can be implemented in a computer system that includes a back-end component, such as a data server, or that includes a middleware component, such as an application server or an Internet server, or that includes a front-end component, such as a client computer having a graphical user interface or an Internet browser, or any combination of them. The components of the system can be connected by any form or medium of digital data communication such as a communication network. Examples of communication networks include, e.g., a LAN, a WAN, and the computers and networks forming the Internet.

The computer system can include clients and servers. A client and server are generally remote from each other and typically interact through a network, such as the described one. The relationship of client and server arises by virtue of computer programs running on the respective computers and 5 having a client-server relationship to each other.

A number of implementations have been described. Nevertheless, it will be understood that various modifications may be made. For example, advantageous results may be achieved if the steps of the disclosed techniques were performed in a different sequence, if components in the disclosed systems were combined in a different manner, or if the components were replaced or supplemented by other components. Accordingly, other implementations are within the scope of the following claims.

What is claimed is:

- 1. A method of correlating power, comprising:
- supplying power to a portion of a location through a power plurality of electrical loads that draw power through a circuit breaker;
- monitoring utilization of a statistical sample of the plurality of electrical loads, the statistical sample being a statistically significant number of the plurality of elec- 25 trical loads that is less than all of the plurality of electrical loads:
- calculating an estimated individual power draw for the statistical sample of the plurality of electrical loads based on the utilization;
- calculating an estimated total power draw for each of a plurality of different times from the estimated individual power draw of the statistical sample of the plurality of electrical loads to generate a plurality of estimated total power draw values for the plurality of different times;
- monitoring actual power draw at the power distribution line by the portion of the location and generating a plurality of actual power draw values for the plurality of different
- determining a functional relationship between pairs of 40 actual power draw values and estimated power draw values, each pair comprising an actual draw value and an estimated draw value for the same time.
- 2. The method of claim 1, further comprising storing data that is descriptive of the functional relationship.
- 3. The method of claim 1, wherein calculating the estimated individual power draw comprises storing data representing an electrical load configuration for each of the plurality of electrical loads, and the electrical load configuration is an input to a function relating utilization to estimated indi- 50 vidual power.
- 4. The method of claim 1, wherein calculating the estimated total power draw comprises summing the estimated individual power draw of the statistical sample of the plurality of electrical loads.
- 5. The method of claim 1, wherein the determining the functional relationship comprises fitting a function to a plurality of pairs of actual power draw values and estimated power draw values, each pair of the plurality of pairs comprising an actual draw value and an estimated draw value for 60 the same time.
 - **6**. The method of claim **1**, further comprising:
 - receiving a maximum power draw value for the portion of the location:
 - calculating an estimated current individual power draw 65 value for the statistical sample of the plurality of electrical loads based on the utilization;

16

- calculating an estimated current total power draw value from the estimated current individual power draw value of the statistical sample of the plurality of electrical loads:
- at least one of
 - calculating an adjusted maximum power draw value from the maximum power draw value and the functional relationship, and comparing the estimated current total power draw value to the adjusted maximum power draw value, or
 - calculating an adjusted estimated current total power draw value from the estimated current total power draw value and the functional relationship, and comparing the adjusted estimated total power draw value to the maximum power draw value; and
- adjusting operation of at least one electrical load in the plurality of electrical loads based on the comparison.
- 7. The method of claim 6, comprising calculating the distribution line, the portion of the location including a 20 adjusted maximum power draw value from the maximum power draw value and the functional relationship, and comparing the estimated current total power draw value to the adjusted maximum power draw value.
 - 8. The method of claim 6, wherein adjusting operation comprises adjusting a multiplicity of electrical loads in the plurality of electrical loads to cause the estimated current total power draw value to be closer to the adjusted maximum power draw value.
 - 9. The method of claim 6, further comprising calculating the adjusted estimated current total power draw value from the estimated current total power draw value and the functional relationship, and comparing the adjusted estimated total power draw value to the maximum power draw value.
 - 10. The method of claim 9, wherein adjusting operation comprises adjusting a multiplicity of electrical loads in the plurality of electrical loads to cause the adjusted estimated total power draw value to be closer to the maximum power draw value.
 - 11. The method of claim 1, wherein calculating an estimated individual power draw for an electrical load of the plurality of electrical loads comprises measuring utilization of the electrical load at a plurality of utilization measurement times to generate a plurality of utilization values for the electrical load, there being a greater number of utilization measurement times than different times such that there are a greater number of utilization values for the electrical load than different times.
 - 12. The method of claim 11, further comprising associating each of the plurality of utilization values with one of the plurality of different times.
 - 13. The method of claim 12, wherein associating includes comparing a utilization measurement time for a utilization value with a midpoint between two adjacent different times.
 - 14. The method of claim 12, wherein there is a plurality of 55 utilization value times for each of plurality of different times.
 - 15. The method of claim 14, wherein calculating the estimated total power draw for one of the different times comprises weighting each estimated individual power draw in the plurality of estimated power draw values based on a difference between a value estimating time for the estimate value and the different time.
 - 16. The method of claim 9, further comprising synchronizing time stamps of the different times and the utilization measurement times.
 - 17. The method of claim 1, wherein calculating an estimated total power draw occurs at a higher frequency than monitoring actual power draw.

- **18**. A power distribution system, comprising: a power distribution line;
- a plurality of electrical loads in a portion of the power distribution system that draw power from the power distribution line through a circuit breaker, wherein each electrical load of a statistical sample of the plurality of electrical loads is configured to generate a measurement of power utilization of the electrical load, the sample being a statistically significant number of the plurality of electrical loads that is less than all of the plurality of electrical loads;
- a processor configured to
 - receive the measurement of utilization from the statistical sample of the plurality of electrical loads,
 - calculate an estimated individual power draw for the statistical sample of the plurality of electrical loads based on the utilization,
 - calculate an estimated total power draw for each of a plurality of different times from the estimated individual power draw of the statistical sample of the plurality of electrical loads to generate a plurality of estimated total power draw values for the plurality of different times,
 - receive a measurement of an actual power draw at the power distribution line by the portion of the power distribution system and generate a plurality of actual 25 power draw values for the plurality of different times, and
 - determine a functional relationship between pairs of actual power draw values and estimated power draw values, each pair of the plurality of pairs comprising an actual draw value and an estimated draw value for the same time.

18

- 19. A non-transitory computer readable medium storing instructions that, when executed by one or more processors, cause the one or more processors to perform operations comprising:
 - receiving the measurement of utilization from a statistical sample of a plurality of electrical loads, the statistical sample being a statistically significant number of the plurality of electrical loads that is less than all of the plurality of electrical loads,
 - calculate an estimated individual power draw for the statistical sample of the plurality of electrical loads based on the utilization,
 - calculate an estimated total power draw for each of a plurality of different times from the estimated individual power draw of the statistical sample of the plurality of electrical loads to generate a plurality of estimated total power draw values for the plurality of different times,
 - receive a measurement of an actual power draw at the power distribution line by the electrical loads and generate a plurality of actual power draw values for the plurality of different times, and
 - determine a functional relationship between pairs of actual power draw values and estimated power draw values, each pair of the plurality of pairs comprising an actual draw value and an estimated draw value for the same time
- 20. The non-transitory computer readable medium of claim 19, wherein the plurality of electrical loads comprises a population of computers in a datacenter.

* * * * *